

45565

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sung-Jin Park et al.

: Group Art Unit:

Serial No.: 10/634,746

: Examiner:

Filed: August 6, 2003

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For: TURBO DECODING APPARATUS

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INFORMATION DISCLOSURE STATEMENT

COMMISSIONER FOR PATENTS

P. O. Box 1450

Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97-1.98, Applicants hereby transmit copies of the documents listed in the attached Form PTO-1449 which may be deemed material to the examination of the above-identified application. A copy of a European Search Report citing these documents are also being transmitted for the Examiner's information.

Since the present Information Disclosure Statement is being filed prior to issuance of a first Office Action on the merits, no certification under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(b) is required.

The Examiner is requested to consider the attached documents in connection with the above-identified application and to return a copy of Form PTO-1449 to the Applicants with the Examiner's initials in the spaces provided.

Submission of this Information Disclosure Statement does not constitute an admission by the Applicants as to the materiality of the attached document to the application, nor do the Applicants waive any right to challenge the validity of the documents as prior art should such action be deemed appropriate.

Respectfully submitted,



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Dated: March 11, 2004

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Sheet 1 of 1

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.	SERIAL NO.
INFORMATION DISCLOSURE CITATION  (Use several sheets if necessary)				45565	10/634,746
				APPLICANT Sung-Jin Park et al.	
				FILING DATE August 6, 2003	GROUP

## U. S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCL ASS	FILING DATE IF APPROPRIATE

## PUBLISHED U. S. PATENT APPLICATIONS

		Pub. No.	Pub. Date	Patentee	CLASS	SUB-CLASS	FILING DATE

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO
		1 2 6 1 1 3 9	11/2002	EP	H03M	13/29	

## OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

	"Bidirectional Shift Registers", <a href="HTTP://WWW.EELAB.USYD.EDU.AU/DIGITAL TUTORIAL/PART2/REGISTER06.HTML">HTTP://WWW.EELAB.USYD.EDU.AU/DIGITAL TUTORIAL/PART2/REGISTER06.HTML</a> , (February 1999)
	Guido Masera et al., "VLSI Architectures for Turbo Codes", <u>IEEE Inc.</u> , Vol. 7, No. 3, pages 369-379 (Sept. 1999)
	Warren J. Gross et al., "Difference Metric Soft-Output Detection: Architecture and Implementation", pages 1-8 (Sept. 2001).
	N. Sklavos et al., "A New Low Power and High Speed Bidirectional Shift Register Architecture", <u>VLSI Design Laboratory</u> , (July 2001)

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.